

FIG. 1

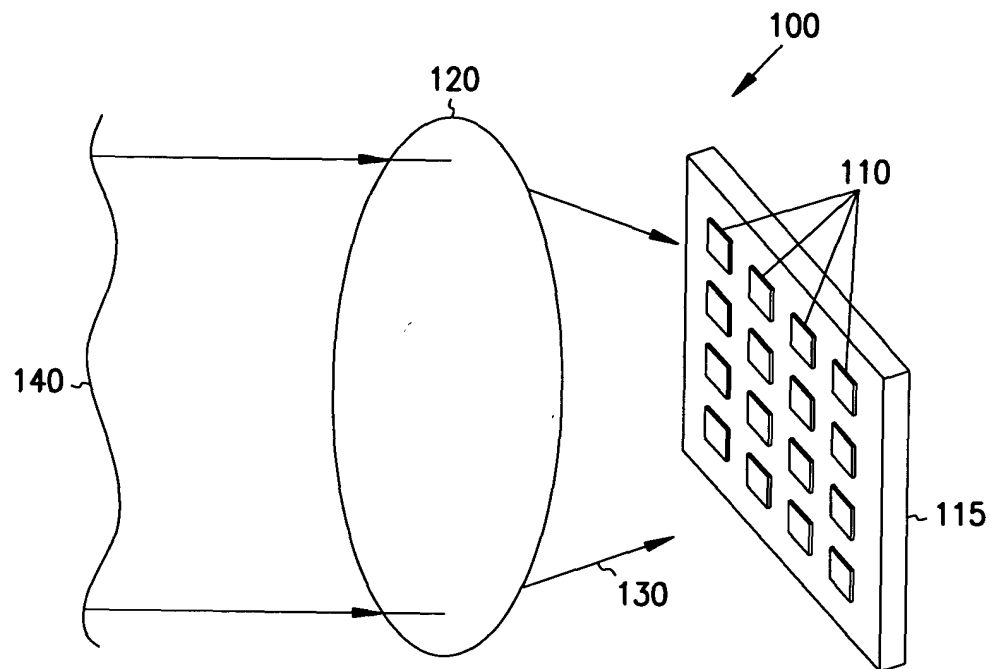


FIG. 1

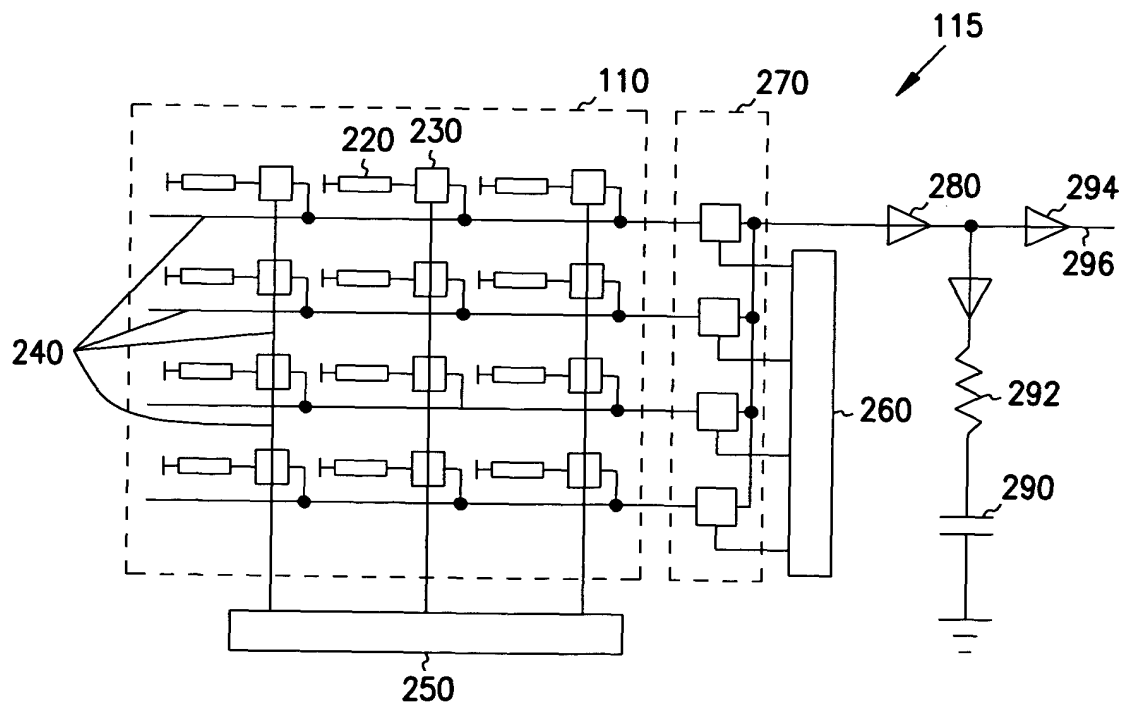


FIG. 2

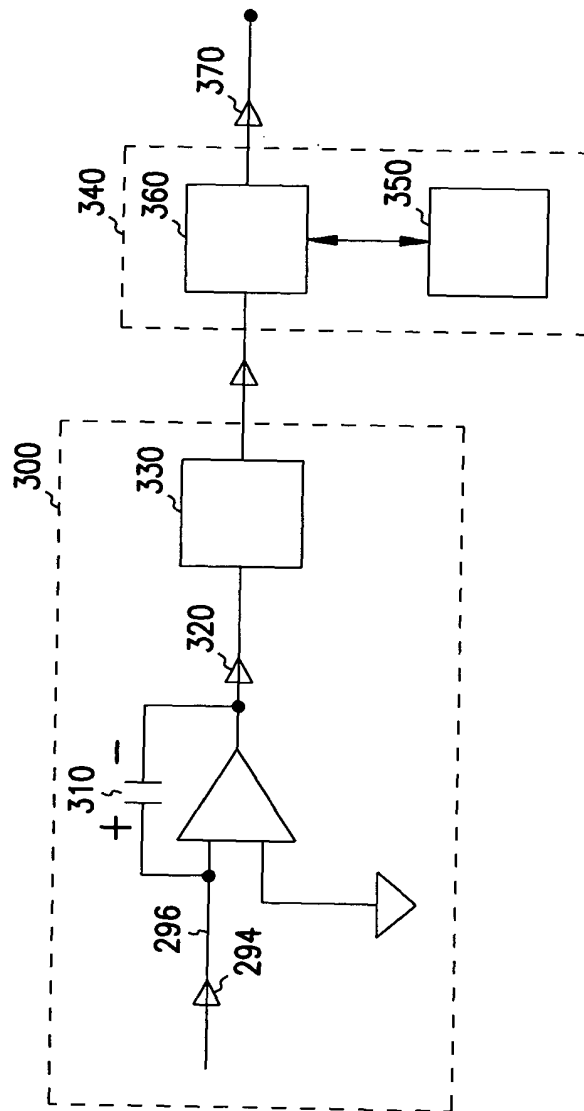


FIG. 3

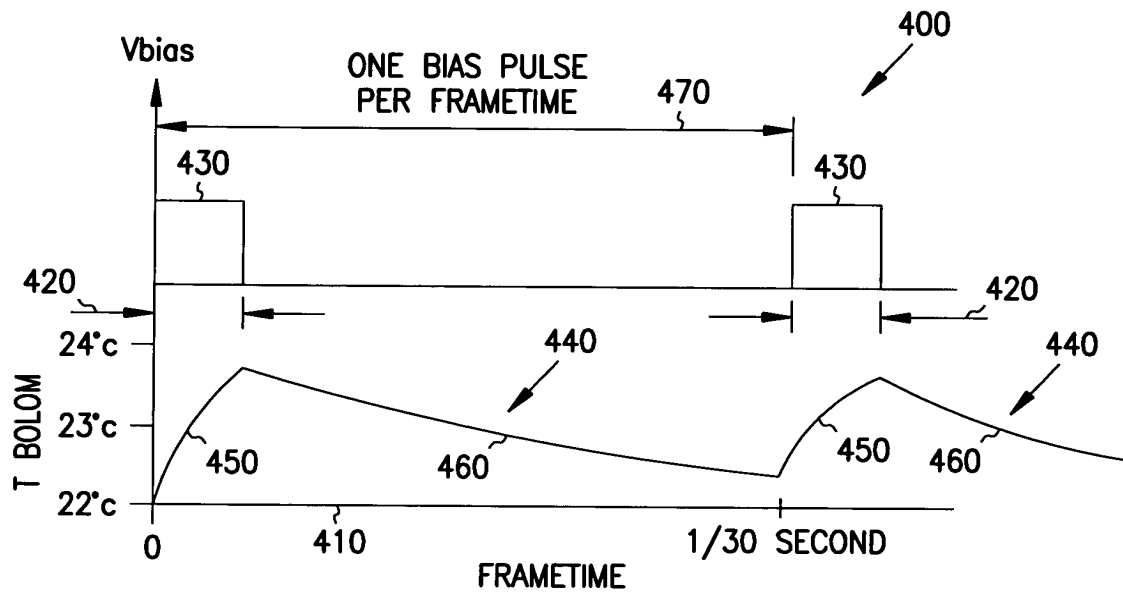


FIG. 4
(PRIOR ART)

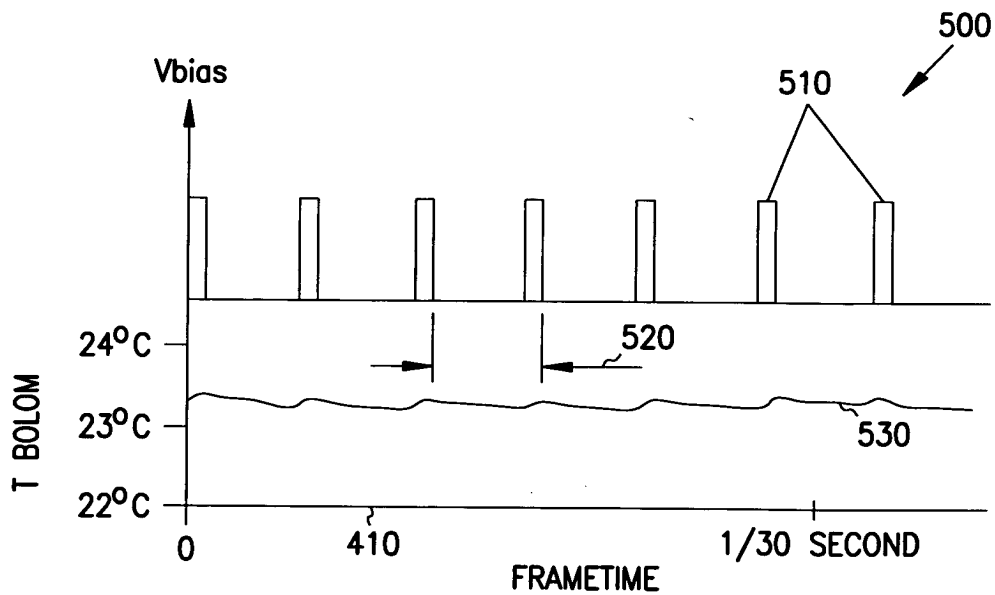


FIG. 5

NEP VS MEASUREMENT

INPUT BIAS CURRENT	CALCULATED NEP
2	1.5E-10
4	6E-11
6	4E-11
8	3.5E-11
10	3.2E-11
12	3E-11
14	2.8E-11
16	2.7E-11
18	2.6E-11
20	2.5E-11

NEP VS MEASUREMENT

INPUT BIAS CURRENT	CALCULATED NEP
2	1.5×10^{-10}
4	6×10^{-11}
6	4×10^{-11}
8	3×10^{-11}
10	2.5×10^{-11}
12	2×10^{-11}
14	1.8×10^{-11}
16	1.6×10^{-11}
18	1.5×10^{-11}
20	1.4×10^{-11}

FIG. 7

60036-0264

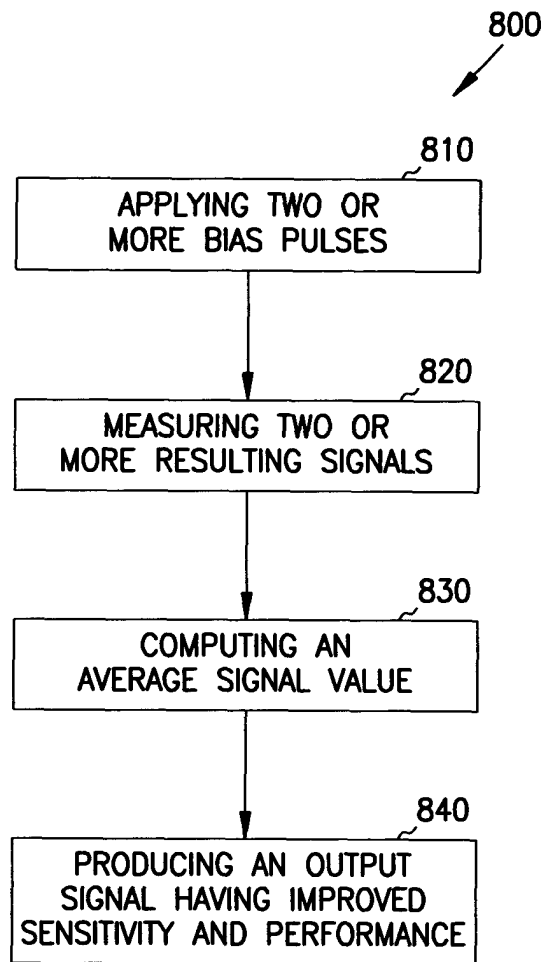


FIG. 8

The diagram illustrates a system 900 for processing microbolometer array data. The system includes a dashed box 115 containing a TIMING CIRCUIT 920 and a MICROBOLOMETER ARRAY 110. The TIMING CIRCUIT 920 provides a control signal to the MICROBOLOMETER ARRAY 110. The MICROBOLOMETER ARRAY 110 outputs a signal to a MEASURING CIRCUIT 930. The MEASURING CIRCUIT 930 outputs a signal to a COMPUTING CIRCUIT 940. The COMPUTING CIRCUIT 940 outputs a signal 296 to an OUTPUT CIRCUIT 300. The OUTPUT CIRCUIT 300 contains an INTEGRATOR AND A/D CONVERTER. The OUTPUT CIRCUIT 300 outputs a signal to a DIGITAL IMAGE PROCESSOR 340. The DIGITAL IMAGE PROCESSOR 340 contains a CORRECTION CIRCUIT 360 and a DIGITAL MEMORY 350. The DIGITAL IMAGE PROCESSOR 340 outputs a UNIFORM OUTPUT SIGNAL VALUE.

```
graph TD
    subgraph 115 [115]
        920[TIMING CIRCUIT]
        110[MICROBOLOMETER ARRAY]
        920 --> 110
    end
    110 --> 930[MEASURING CIRCUIT]
    930 --> 940[COMPUTING CIRCUIT]
    940 -- 296 --> 300
    subgraph 950 [950]
        subgraph 300 [300]
            300[OUTPUT CIRCUIT]
            300 --> 350
            subgraph 350 [350]
                350[INTEGRATOR AND A/D CONVERTER]
            end
        end
    end
    350 --> 340
    subgraph 340 [340]
        subgraph 340 [340]
            340[DIGITAL IMAGE PROCESSOR]
            340 --> 360
            340 --> 350
            subgraph 360 [360]
                360[CORRECTION CIRCUIT]
            end
            subgraph 350 [350]
                350[DIGITAL MEMORY]
            end
        end
    end
    360 --> 350
    350 --> 350
    350 --> 350
```

FIG. 9